



## **NEWS RELEASE**

### **Michael Steinberger, SiSoft Lead Architect, Wins Engineer of the Year**



MAYNARD, MA – January 28, 2015 -- Signal Integrity Software Inc. (SiSoft) today announced that Michael Steinberger, Ph.D., Lead Architect of Serial Channel Products for the company, has been selected as DesignCon Engineer of the Year 2015. The winner was selected based upon leadership skills, creativity and out-of-the-box thinking brought to the design/test of chips, boards or systems with particular emphasis paid to the areas of signal and power integrity. The award, sponsored by National Instruments and presented as part of the DesignCon Best in Design & Test Awards, was presented earlier today at DesignCon 2015 which is being held January 27-30, 2015, at the Santa Clara Convention Center.

"I am very happy to see Mike win the DesignCon Engineer of the Year. He is a dedicated member of the DesignCon Technical Program Committee and clearly shows his dedication to continuing education," said Janine Love, DesignCon Technical Program Director. "My hearty congratulations go out to all the finalists, and, of course, to Mike in particular."

"We are thrilled and extremely proud to see Mike win this award," said Barry Katz, SiSoft president and CTO. "It is wonderful to see Mike recognized for his contributions to the entire signal integrity community and to engineers as a whole through the sharing of his knowledge in the technical papers he authors, the committees he serves on and his mentorship to other engineers."

### **About SiSoft**

SiSoft collaborates with customers and their suppliers to develop innovative solutions to the world's toughest high-speed design problems. SiSoft accelerates design cycles through a combination of award-winning EDA simulation software, methodology training and consulting services. Quantum Channel Designer (QCD) is the industry's premier channel simulator for the design and analysis of Multi-Gigabit serial links and a DesignVision Award Winner. Quantum-SI (QSI) is the leading solution for integrated signal integrity, timing and crosstalk analysis of high-speed parallel interfaces. SiSoft's products automate comprehensive pre- and post-route analysis of high speed interfaces, detailing a design's operating voltage and timing margins. More information on SiSoft can be found at [www.sisoft.com](http://www.sisoft.com).

### **About DesignCon**

DesignCon, produced by UBM Tech, is the world's premier conference for chip, board and systems design engineers in the high-speed communications and semiconductor communities. DesignCon, created by engineers for engineers, takes place annually in Silicon Valley and remains the largest gathering of chip, board and systems designers in the country. This four-day technical conference and expo combines technical paper sessions, tutorials, industry panels, product demos and exhibits from the industry's leading experts and solutions providers. More information is available at: [www.designcon.com/santaclara](http://www.designcon.com/santaclara)

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